

U.S. Application No. 09/900,224, filed July 6, 2001
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Amendment (via EFS-WEB) dated December 31, 2007
In Response to Office Action mailed July 31, 2007

Amendments to the Specification

In the specification, please replace the paragraph on page 4, lines 20-34 with the following amended paragraph:

FIG. 1 is a block diagram of a parameter concealment embodiment of the present invention. A first device 10 generates and transmits a parameter signal 14 and a control signal 16 to a second device. The second device includes a parameter concealment logic block used to conceal device input parameters passed between the two devices. The parameter concealment logic block receives the parameter signal and the control signal and performs a transformation on the parameter signal according to the control signal in order to generate a destination parameter signal 20. The destination parameter signal is transmitted to destination registers 22 within the device. In one embodiment of a parameter concealment logic block according to the present invention, the destination parameter signal in the destination registers is transmitted as a feedback signal 24 to be reused in the parameter concealment logic block.